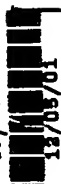


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30474 U.S. PTO
09/683243



	Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

Best Available Copy
U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>LH3</i> <i>U.A.</i>	

APPLICATION NO. 09/683243	CONT/PRIOR D	CLASS 326 <i>713</i>	SUBCLASS <i>300</i>	ART UNIT 2819	EXAMINER <i>2115</i> <i>Wang</i>
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APPLICANTS

Chia-Hsing Yu
Nai-Shung Chang

TITLE

Integrated level shift and override circuit for a processor system

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL CLASSIFICATION									
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
				NOTICE OF ALLOWANCE MAILED	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____				ISSUE FEE	
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